## **EDITORIAL**

## Foreword

The collection of papers that follow this Editorial (beginning on page 43) come from the symposium "Computing Futures in Engineering Design" that was held on the campus of Harvey Mudd College in Claremont, California, on May 2–3, 1997. The symposium focused on *future* roles of computing in *doing* design and engineering and in the *teaching* of design and engineering. The intention of the symposium organizer, Clive Dym, was to provide useful insight, advice, and information to educators about how they might think about the future of design in engineering education.

While these peer-reviewed "Position Papers" are shorter, and perhaps not as formal as papers typically published in *AIEDAM*, they are provocative. Education is central to our role as researchers; indeed, as researchers we aim to educate our colleagues. It is important, however, to consider specifically the pedagogical role that computers play in the teaching of design, in all its many forms. It is our hope in publishing these papers that we can get the research community to discuss issues of education, and to consider the pedagogical implications of our work.

William P. Birmingham, Editor Clive L. Dym, Editor Emeritus

## **ACKNOWLEDGMENTS**

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