3D Integrated Circuits: Materials Challenges

Wednesday, March 25 | 12:00 pm - 1:30 pm (ET)

The fabrication of mobile and other electronic devices by three-dimensional integrated circuits (3D ICs) is receiving wide attention. The concept of using 3D ICs to extend the limit of Moore’s Law, by combining chip technology and packaging technology, has been explored for more than 10 years. However, we still do not mass produce 3D IC devices due to low yield and reliability, along with high cost. Most problems are caused by materials selection and integration at the small scale. The presentations in this webinar will cover some of the important aspects of materials challenges in 3D ICs.

This webinar expands on research that is featured in the March 2015 Issue of MRS Bulletin.

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