A HEURISTIC METHOD FOR THE DETERMINATION OF A HAMILTONIAN CIRCUIT IN A GRAPH

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Abstract

A heuristic methodology for the identification of a circuit passing through all the vertices only once in a graph is presented. The procedure is based upon defining a normal form of a matrix and then transforming the adjacency matrix into its normal form. For a class of graphs known to be Hamiltonian, it is conjectured that this methodology will identify circuits in a small number of steps and in many cases merely by observation.

Introduction

A circuit in a connected graph is said to be Hamiltonian if it includes every vertex once. Hence a Hamiltonian circuit in a graph of n vertices consists exactly of nedges. The problem of finding an efficient algorithm for identifying a Hamiltonian circuit in a graph has offered an interesting challenge to the applied mathematicians and graph theorists since it was first proposed by Sir William Rowan Hamilton in 1895. The basic unvolved problem of determining which graphs are Hamiltonian has led to the investigation of many related problems, and consequently, to the development of what might generally be termed, "Hamiltonian theory". Since it has been shown [3, 7] that the general problem for finding a Hamiltonian circuit in a graph is *NP*-complete, attention has been directed to the development of a Hamiltonian circuit have been obtained in terms of the degree sequence of a graph. Polynomial-time algorithms have been given for finding a Hamiltonian cycle in a graph satisfying such

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sufficient conditions. In this paper some known results regarding Hamiltonian circuits are reviewed and a heuristic algorithm is presented to determine a Hamiltonian circuit in a graph.

Preliminaries

Standard graph theory terminology [1, 2, 5] is used. Formally, a graph G = (V, E) consists of a finite nonempty set of vertices V and a set of edges E. If the edges are ordered pairs (u, v) of vertices, then the graph is said to be *directed*. If the edges are unordered pairs of distinct vertices also denoted by (u, v), then the graph is said to be *undirected*. A directed graph is also called a *digraph*. The order of a graph n denotes the number of vertices of G: n = |V|.

An edge uv is said to join the vertices u and v and is denoted by uv. If $uv \in E$ then u and v are *adjacent* and the vertices u and v are *incident* with edge uv.

The size of a graph e is the number of edges i.e. e = |E|. A graph of order n and size $\binom{n}{2}$ is called a *complete graph* and is denoted by K_n . In K_n every two vertices are adjacent.

Simple graph. A graph is simple if there is no loop of length one at any one of the vertices.

A digraph is *strict* if it has no loop and no two edges with the same vertices have the same orientation.

A graph is bipartite if the vertex set V can be partitioned into two subsets V_1 and V_2 such that every edge of G joins V_1 with V_2 .

The degree of a vertex v in a graph G is the number of edges of G incident with v. In a digraph the outdegree od(v) of a vertex v is the number of vertices adjacent to it. A walk of a graph G is an alternating sequence of vertices and edges $v_1E_1v_2E_2 \cdots E_kv_k$, beginning and ending with points in which each edge is incident with the two points immediately preceding and following it. This walk joins v_1 and v_k and may also be denoted by $v_1v_2 \cdots v_k$; it is sometimes called a v_1v_k walk. The walk is closed if $v_1 = v_k$ and is open otherwise. A path is a walk where all the vertices are distinct. A circuit or a cycle is a walk $v_1v_2 \cdots v_k$ where all the vertices are distinct except for $v_1 = v_k$. The length of a walk is the number of occurrences of edges in it. A digraph is diconnected if for any two vertices u and v there is a directed path from u to v. A graph is connected if for every pair (u, v) of distinct vertices there is a path from u to v.

The k-closure $C_k(G)$ of a simple graph G of order n is the graph obtained from G by recursively joining pairs of nonadjacent vertices with degree at least k.

The adjacency matrix $A = [a_{ij}]$ of G is the $n \times n$ matrix given by

$$a_{ij} = \begin{cases} 1 & \text{if } v_i v_j \in E \\ 0 & \text{otherwise} \end{cases}$$

Sufficient conditions for a Hamiltonian graph

Even though no elegant characterization of a Hamiltonian graph has yet been found there have been several sufficient conditions established for a graph to be Hamiltonian. Chronologically, Dirac, Ore, Posa, Bondy and Chvatal have determined such conditions in terms of the degrees of the vertices of a graph, with each successive result strengthening those preceding it [8]. A graph G of order $n \ge 3$ with degree sequence $d_1 \le d_2 \cdots \le d_n$ is Hamiltonian if:

- (i) (Dirac) $1 \le k \le n \Rightarrow d_k \ge n/2$
- (ii) (Ore) $uv \notin E \Rightarrow \deg u + \deg v \ge n$
- (iii) (Posa) $1 \le k < n/2 \Rightarrow d_k > k$



Figure 1a. A Hamiltonian graph which does not satisfy the sufficient conditions of Posa, Ore, Dirac.



Figure 1b. A Hamiltonian graph which does not satisfy the condition of Las Vergnas.

Figure 1. Hamiltonian graphs which do not satisfy sufficient conditions.

- (iv) (Bondy) $j < k, d_i \leq j, d_k \leq k 1 \Rightarrow d_i + d_k \ge n$
- (v) (Chvatal) $d_k \le k \le n/2 \Rightarrow d_{n-k} \ge n-k$
- (vi) (Las Vergnas) A graph G of order $n \ge 3$ is Hamiltonian if the vertices can be labeled v_1, v_2, \ldots, v_k so that

$$\begin{cases} j < k, \ k \ge n - j, \ v_j v_k \notin E \\ \deg v_j \le j, \ \deg v_k \le k - 1 \end{cases} \Rightarrow \deg v_j + \deg v_k \ge n$$

- (vii) If G is a graph of order n and the n closure $C_n(G)$ is complete, then G is Hamiltonian. It can be shown [8] that conditions (i)-(vi) guarantee that $C_n(G)$ is complete. However, (vii) is strictly stronger than all of these conditions.
- (vii) (Meyniel) In a strict diconnected digraph G, if $uv \notin E$ and $deg(u) + deg(v) \ge 2n 1$ for all $u, v \in V$ then G contains a direct Hamiltonian circuit.

The above conditions are sufficient but not necessary for the existence of a Hamiltonian circuit in a graph. For example the graph shown in Figure 1a is Hamiltonian yet it does not satisfy the conditions (i), (ii) and (iii). The graph shown in Figure 1b is Hamiltonian by (vii) but it does not satisfy the condition (vi).

Conditions in terms of edges

Related results involving the degree of the vertices of a graph are those involving the number of edges of the graph. Ore [9] showed that G is Hamiltonian if $n = |V| \ge 3$ and $|E| \ge (n^2 - 3n + 6)/2$. Another result due to Posa [10] states that there exists a constant c such that the probability that a random graph with n vertices and $cn \log n$ edges is Hamiltonian tends to 1 as $n \to \infty$.

Necessary condition

Since a Hamiltonian circuit must pass through each of the vertices, a simple necessary condition can be obtained [6] in terms of the power of the adjacency matrix of G: Let $A^n = [a_{ij}^{(n)}]$. Since $a_{ij}^{(n)} > 0$ implies there exists a walk of length n between v_i and v_j , the necessary condition for the existence of a circuit of length n at each of the vertices is that $a_{ij}^{(n)} > 0$ for all $i \leq n$.

Heuristic method

A heuristic may be viewed as a less-than-perfect method [4] which usually finds a solution. In this paper the word heuristic is used in the sense of a method for guiding discovery or improving problem solving. In order to develop the method to determine a Hamiltonian circuit in G by heuristic, the following definitions will be essential.

Normal form of a matrix: Let n be the dimension of the adjacency matrix A and h be an integer such that $1 \le h \le n - 1$. For all $i, 1 \le i \le n$ if $A(i, \text{mod}_n(i + h)) = 1$ with the modification that $\text{mod}_{\hat{h}}(n) = n$, then A is said to have a normal form. In this paper the normal form is taken to be of first order i.e. h = 1.

Normal path of a matrix. The path made up of the off-diagonal elements of a matrix, i.e. made up of $(n - 1) a_{ii+1}$ elements such that $1 \le i \le n - 1$.

The anchor of a matrix. The element corresponding to a_{n1} .



Figure 2a. Matrix N is in normal form.



Figure 2b. Matrix M is not in normal form.

Figure 2. Normal form of a matrix.

Hamiltonian circuit in a graph



Figure 3a. Matrix P is transformed into matrix Q.



Figure 3b. Matrix R cannot be transformed into normal form.

Figure 3. Transformation into normal form.

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Note in the definitions of normal form, normal path and anchor that the other elements of the matrix are of no consequence. The matrix N shown in Figure 2a is in normal form while the matrix M shown in Figure 2b is not in normal form. If a matrix is not in normal form, attempts can be made to transform it into normal form by interchanging any set of pairs of columns and rows, even though there is no guarantee that a matrix which is initially not in normal form can ultimately be transformed into normal form. For example the matrix P in Figure 3a is not in normal form. But by first interchanging column 3 and column 4 and then by interchanging corresponding row 3 and 4, P is transformed into Q as shown in Figure 3b which is in normal form. But no amount of interchanging of pairs of columns and rows will transform the matrix R shown in Figure 3c into a normal form.

Observation. If a Hamiltonian circuit exists in a graph G then its adjacency matrix must be transformable into normal form, and the row or column headings will identify the circuit. Alternatively, for graphs which are known to be Hamiltonian the corresponding adjacency matrices can always be transormed into normal forms.

A heuristic algorithm to determine a Hamiltonian circuit in a graph

- 1. Obtain the adjacency matrix $A = [a_{ij}], a_{ii} \leftarrow 0 \ i = 1, 2, ..., n$; i.e. make the graph simple.
- 2. Identify each column and each row by the headings C_i and R_i . Initially each C_i and each R_i corresponds to each v_i and has the value i, i = 1, 2, ..., n.
- 3. Let the set V, contain all the vertices adjacent to the vertex v_i ; $v_i \notin V_i \subset V$.
- 4. If A is in normal form then the Hamiltonian Circuit (HC) is identified as $v_1, v_2 \cdots v_k$. Otherwise, go to 5.
- 5. Proceed from left to right along the normal path. If a zero element is encountered, set the corresponding column heading as C_i . Replace this zero element by any nonzero element from the same row corresponding to the column heading C_j . Note: the replacement of the zero element is done by the interchanging of columns corresponding to C_i and C_j and rows corresponding to R_i and R_j . While performing this interchanging of rows and columns, there is a possibility that some existing nonzero elements in the normal path may be removed. The index j should be chosen in such a way that the removal of the nonzero elements becomes minimal. This situation corresponds to the "least disturbed" condition of the normal path.
- 6. If the anchor, i.e. the element corresponding to R_nC_1 is zero, replace it with a nonzero element so that the normal path is least disturbed.

- 7. If the matrix is now in normal form go to 8 otherwise go to 5.
- 8. Identify the *HC* by the vertices corresponding to the column headings $C_1, C_2 \cdots C_n$.

Illustrative examples

In order to demonstrate the algorithm, the following two examples are worked out in detail. For the first example it is not known in advance whether the graph contains any Hamiltonian cycle. But for the second example [11] it is known in advance that the graph is Hamiltonian since it satisfies Meyniel's condition.

Example 1.

Consider the graph of Figure 4. Steps 1 and 2:



Figure 4. Graph of Example 1.

Step 3:

$$v_1: V_1 = \{v_2, v_3\}$$

$$v_2: V_2 = \{v_4\}$$

$$v_3: V_3 = \{v_1, v_2\}$$

$$v_4: V_4 = \{v_3, v_5\}$$

$$v_5: V_5 = \{v_1, v_2, v_3, v_4\}$$

Step 4: A is not in normal form.

Step 5: The first zero element encountered corresponds to C_3 , choose $v_j \in V_2$, here j = 4. Interchange columns corresponding to C_3 and C_4 and rows corresponding to R_3 and R_4 . A is thus transformed into A_1 .

The second zero encountered corresponds to C_5 . Since $V_3 = \{v_1, v_2\}$, by replacing columns corresponding to C_5 and C_1 the normal path is least disturbed. A_1 is transformed into A_2 .

The normal path is now made up of 5-1 = 4 nonzero elements.

Step 6: The anchor corresponding to $R_5C_1 = 0$. Since $V_1 = \{v_2, v_3\}$ and the objective is to disturb the normal path the least, interchange columns corresponding to C_1 and C_4 and rows corresponding to R_1 and R_4 . A_2 is now transformed into A_3 .

Step 7: The matrix A_3 is now in normal form.

Step 8: A Hamiltonian circuit is obtained as follows:

$$v_3 v_2 v_4 v_5 v_1 v_3$$

Example 2: Consider the graph in Figure 5. Its adjacency matrix A is shown below.

		1	2	3	4	5	6	7	8	9	10	11
<i>A</i> =	1	Γo	1	1	1	0	0	0	1	1	0	1
	2	0	0	1	0	0	1	1	1	0	1	1
	3	0	0	0	1	1	0	0	0	0	1	1
	4	1	1	1	0	1	0	1	1	0	0	1
	5	0	1	1	1	0	1	0	0	0	0	1
	6	0	0	1	0	1	0	1	1	0	0	1
	7	1	1	1	1	0	1	0	1	0	0	1
	8	1	1	1	0	0	0	1	0	0	0	1
	9	1	0	0	0	1	1	1	1	0	1	1
	10	1	1	0	0	1	1	0	0	1	0	1
	11	0	0	0	0	0	0	0	0	1	1	0_

A is almost in normal form except in column 9, and in the anchor position. Since $v_{11} \in V_8$, v_{11} is a potential candidate to be brought into the normal path. By interchanging the columns 9 and 11 and the corresponding rows 9 and 11, A is transformed into A_1 which is in the normal form.

A Hamiltonian circuit is identified as follows:

 $v_1v_2v_3v_4v_5v_6v_7v_8v_{11}v_{10}v_9v_1$



Figure 5. Graph of Example 2.

Concluding remarks

In conclusion several characteristic features of the proposed algorithm are noted. The algorithm is applicable for both directed and undirected graphs. The essential element of the algorithm is first to define a normal form of the adjacency matrix and then try to transform the matrix into its normal form. The normal form is obtained by interchanging pairs of columns and corresponding pairs of rows. Thus the most important aspect of the algorithm is to determine the best possible candidate to be interchanged with. Otherwise, there is a potential risk that these interchanges will result in the method cycling in a loop. In selecting the "best candidate" some looking-ahead aspect should be introduced so that the "least disturbance" here does not cause a major disturbance later. However, there is no efficient algorithm known to determine the best candidate. If the adjacency matrix of a Hamiltonian graph is crowded with 1's, then it is quite likely that it can be transformed into a normal form in some smaller number of steps and in many cases merely by observation. The adjacency matrices of the graphs which satisfy the sufficient conditions as stated by Ore, Dirac and Meyniel are very dense with 1's. Thus it is postulated that for the above situations, the complexity of the determination of a Hamiltonian Circuit is 0(n).

References

- [1] A. V. Aho, J. E. Hopcroft, and J. D. Ullman, *The design and analysis of computer algorithms* (Addison-Wesley, 1974).
- [2] B. Bollobas, Graph Theory (Springer-Verlag, 1979).
- [3] M. R. Gary, D. S. Johnson and R. E. Tarjan, "The planar Hamiltonian circuit problem is NP complete", SIAM J. Comput. 5 (1976), 704-714.
- [4] R. Groner, H. Groner and W. F. Bischof, *Methods of Heuristics* (Lawrence Erlbaum Asso. 1983).
- [5] F. Harary, Graph Theory (Addison-Wesley, 1972).
- [6] S. B. Karmakar, "Detection of Hamiltonian circuits in a directed graph", J. Austral. Math. Soc. Ser. B 24 (1982), 234-242.
- [7] M. S. Krishnamoorthy, "An NP hard problem in bipartite graphs", SIGACT New 7 (1975), 1-2C.
- [8] L. Lesniak-Foster, "Some recent results in Hamiltonian graphs", J. Graph Theory 1 (1977), 27-36.
- [9] O. Ore, "Arc coverings of graphs", Ann. Mat. Pura Appl. 55 (1961), 315-322.
- [10] L. Posa, "Hamiltonian circuits in random graphs", Discrete Math. 14 (1976), 359-364.
- [11] K. Takamizawa, T. Niskizehi and N. Saito, "An 0(p³) algorithm for finding Hamiltonian cycles in certain digraphs", J. Inform. Process, 3 (1980), 68-72.

[12]